

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hirokazu Oki et al. Art Unit : Unknown  
Serial No. : 10/599,177 Examiner : Unknown  
Filed : September 21, 2006 Conf. No. : 5429  
Title : SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND SWITCHING  
POWER SOURCE DEVICE USING THE SAME

**MAIL STOP AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT**

Please consider the references listed on the enclosed PTO-1449 form. Copies of foreign patent documents are enclosed; copies of cited U.S. patents and patent application publications will be provided on request.

This statement is being filed before the first Office action on the merits. All the references contained in this statement were cited in a communication from the European Patent Office in a counterpart application, the communication being dated April 25, 2008, which is not more than three months prior to the filing of this statement.

An English-language abstract and translation are enclosed for reference AH.

An English-language abstract is enclosed for reference AI.

Reference JP62-156849 is mentioned in the European Search Report and has been previously submitted with an Information Disclosure Statement filed with the application on September 21, 2006.

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Attorney's Docket No.: 19415-018US1 / PCT-05R-  
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Respectfully submitted,

Date: 7/1/08

  
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